

□ □ — □ □ □ □ □
□ □ □ □ □ □ □ □ □ □
□ □ □ □ □ □ □ □ □ □ □ □ □ □ □
□ □ □ □ □ □ □ □ □ □ □ □

□ □ □ □ □ □ □ □ □ □ □ □ □ □
□ □ □ □ □ □ □ □ □ □ □ □ □ □

□ □ □ □

[illegible]

-

[illegible]

0120- 25- 3047
042- 351- 6617

[illegible]

第一章 緒 論	1
第二章 概 論	2
2.1 概 論	2
2.2 概 論	2
2.3 概 論	2
2.4 概 論	3
2.4.1 概 論	3
2.4.2 概 論	4
2.5 概 論	5
第三章 概 論	6
3.1 概 論	6
3.2 概 論	7
3.2.1 概 論	7
3.2.2 概 論	8
第四章 概 論	9
4.1 概 論	9
4.2 概 論	10
4.2.1 概 論	13
4.3 概 論	16
4.4 概 論	17
4.4.1 概 論	17
4.4.3 概 論	19
4.4.4 概 論	19
4.4.5 概 論	19
4.4.6 概 論	19
4.4.7 概 論	19
4.4.8 概 論	19
4.4.9 概 論	20
4.4.10 概 論	20
4.4.11 概 論	20
4.4.12 概 論	21
4.4.13 概 論	21
4.4.14 概 論	21
4.4.15 概 論	22
4.4.16 概 論	22
4.4.17 概 論	22

4. 4. 18	□ □ □ □ □ □ □ □ □ □	23
4. 4. 19	□ □ □ □ □ □ □ □ □ □	24
4. 4. 20	□ □ □ □ □ □ □ □ □ □	25
4. 4. 21	□ □ □ □ □ □ □ □ □ □	26
□ □ □ □ □		27
5. 1	□ □ □ □ □ □ □ □	27
5. 2	□	27
5. 3	□ □ □ □ □ □	29
5. 4	□ □ □ □ □ □ □	29
5. 5	□ □ □ □ □ □ □ □	30
5. 6	□ □ □ □ □ □ □ □ □ □	31
5. 7	□ □ □ □ □ □ □ □	33
5. 8	□ □ □ □ □ □	33
5. 9	□ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □	36
□ □ □ □ □ □ □ □ □ □ □ □		37
6. 1	□ □ □ □ □ □	37
6. 2	□ □ □ □ □	37
6. 3	□ □ □ □ □ □ □ □ □ □ □	39
6. 4	□ □ □ □ □ □	41
6. 4. 1	□ □ □ □ □ □ □ □ □ □	41
6. 4. 2	□ □ □ □ □ □ □	41
6. 4. 3	□ □ □ □ □ □ □	41
□ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □		44
□ □ □ □ □ □ □ □		44
□ □ □ □ □ □ □ □ □ □		45
□ □ □ □ □ □ □ □ □ □		164

3.2 编译选项和编译选项

3.2.1 编译选项和编译选项

编译选项和编译选项
<path> 编译选项和编译选项 <file> 编译选项和编译选项

(1) 编译选项

```
C>ch38 /debug /cpu=300ha <file>
debug 编译选项和编译选项
编译选项和编译选项
```

(2) 编译选项

```
C>nk <file> /debug /lib=a:¥ch38¥lib¥c38ha.lib [/entry=<编译选项>]
entry 编译选项和编译选项
编译选项和编译选项
编译选项和编译选项
编译选项和编译选项
entry 编译选项和编译选项
```

```
C>cesym<file>
icesym 编译选项和编译选项 .ice 编译选项和编译选项
编译选项和编译选项
```

(3) 编译选项

```
编译选项和编译选项
>oad <file>
编译选项和编译选项 entry 编译选项和编译选项
```

(4) 编译选项

```
编译选项和编译选项 sample.c 编译选项和编译选项
```

```
C>ch38 /debug /cpu=300ha sample.c
编译选项和编译选项 sample.obj sample.lst
C>nk sample.obj /debug /entry=_main /lib=a:¥ch38¥lib¥c38ha.lib
编译选项和编译选项 /lib 编译选项和编译选项
编译选项和编译选项
编译选项和编译选项 sample.abs
C>cesymsample
编译选项和编译选项 sample.ice
```

```
ICE 编译选项和编译选项
>cesrc a:¥src 编译选项和编译选项
>set mode/1
>map 0 ffff eram
.....
>oad sample
编译选项和编译选项 main() 编译选项和编译选项
编译选项和编译选项
```

3.2.2 编译选项

编译选项在编译时通过命令行或配置文件指定。

(1) 编译选项

```
C>as388 <file> /debug /cpu=300ha
debug 编译选项
编译选项
```

(2) 编译选项

```
C>nk <file> /debug [/entry=<编译选项>]
编译选项 /entry 编译选项
编译选项 /entry 编译选项
编译选项
```

```
C>icesym <file>
icesym 编译选项 .ice 编译选项
编译选项
```

(3) 编译选项

编译选项

```
>icesrc <path> 编译选项
>load <file>
load 编译选项
编译选项 /entry 编译选项
```

(4) 编译选项

编译选项 sample.src 编译选项

```
C>as388 sample.src /debug /cpu=300ha
编译选项 sample.obj sample.lst
```

```
C>nk sample.obj /debug /entry=start
编译选项 sample.abs
```

```
C>icesymsample
编译选项 sample.ice
```

编译选项

```
>icesrc a:¥src 编译选项
```

```
>set mode/1
```

```
>nap 0ffff eram
```

.....

```
>load sample
```

```
编译选项 start 编译选项
编译选项
```


4. 2. 1 ☐ ☐ ☐ ☐ ☐

[illegible]

(1) □ □ □ □ □ □ □ □

$\frac{1}{n} \sum_{i=1}^n x_i = \bar{x}$

[illegible][illegible]

(2) ☐ ☐

[illegible]

(3) ☐ ☐

[illegible][illegible][illegible]

QUESTION

(4) □ □ □ □ □ □ □

(a) ☐ ☐ ☐ ☐ ☐ ☐

[illegible]

```

J:¥WICE
J:¥WICE,J:¥WSIM

```

```

-section=p=PROG
-section=p=PROG -define=DEF1

```

(b)

```


```

```

J:¥WICE
J:¥WICE,J:¥WSIM

```

```

-list=c:¥est¥asm1st
-list=c:¥est¥asm1st -section

```

(c)

```

P(1000),C(0D000)

```

```

[ ] P,D(400),R,B(10000)
[ROM] (D,R)

```

```


```

```

_nai n

```

```


```

```

-define=SYM1(10) -define=SYM2(20)

```

(d)

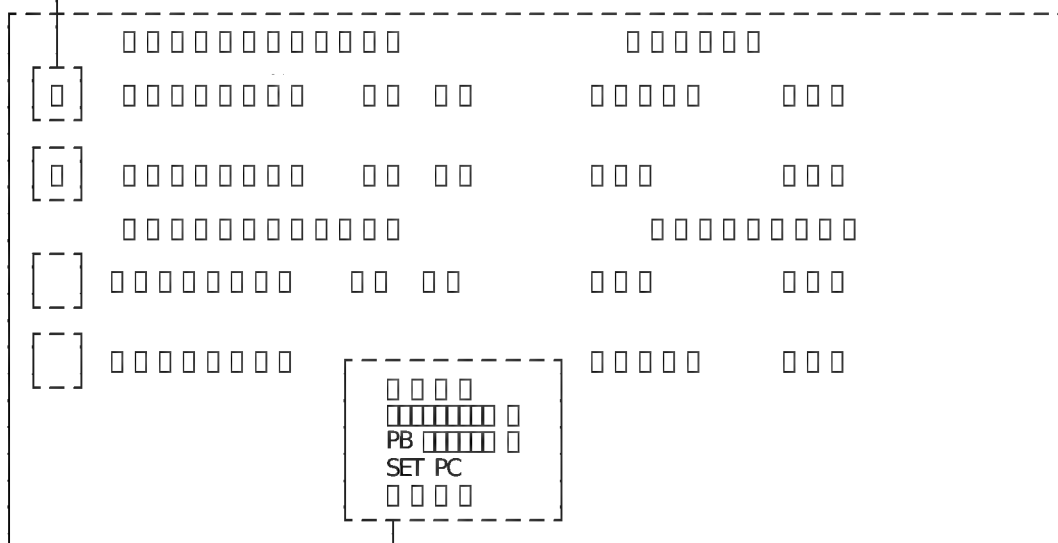
(e)

```


```


[illegible]

```
PC9801   ROLL UP    ROLL DOWN      Page Up     Page Down  
-       
```

[illegible][illegible][illegible]

<div><div><div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div></div><div><div><div></div><div></div><div></div><div></div></div></div></div></div>	<div><div><div></div><div></div></div></div>
<div><div><div><div></div><div></div><div></div><div></div></div><div><div><div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div></div></div></div></div></div>	

4.4.12 数据类型转换

数据类型转换函数用于将一种数据类型转换为另一种数据类型。常用的转换函数包括：

4.4.13 字符串函数

字符串函数用于对字符串进行操作。常用的字符串函数包括：

字符串函数包括：DECIMAL(U) 将字符串转换为十进制数，DECIMAL(S) 将字符串转换为十进制数，HEX 将字符串转换为十六进制字符串，BINARY 将字符串转换为二进制字符串，ASCII 将字符串转换为 ASCII 码。

4.4.14 日期函数

日期函数用于对日期进行操作。常用的日期函数包括：

日期函数包括：DATE 将字符串转换为日期，TIME 将字符串转换为时间，TIMESTAMP 将字符串转换为时间戳，YEAR 提取年份，MONTH 提取月份，DAY 提取日期，HOUR 提取小时，MINUTE 提取分钟，SECOND 提取秒数，WEEK 提取周数，QUARTER 提取季度，DAYOFWEEK 提取星期几，DAYOFMONTH 提取月份中的第几天，DAYOFYEAR 提取年份中的第几天，TIMEZONE 提取时区。

```

PC9801 - ROLL UP ROLL DOWN Page Up Page Down

```

[illegible]

□ □ - □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □

[illegible][illegible]

PER

4. 4. 18

(D) (I)

INSPECT

-

-

Repai nt	
Expand	()
Range	
Change	
Close	

(Expand)

Range (: TOP= 4, END=7) TOP=0 END=0

4. 4. 20 □ □ □ □ □ □ □ □ □ □

[illegible]

[illegible]

29

[illegible][illegible]

6. 4. 3 □ □ □ □ □ □ □

(1) 

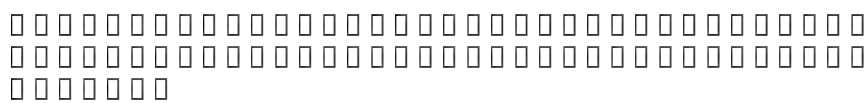
FU/



FULL



41

[illegible]

The diagram illustrates the assembly of a 16-bit PBO register. It begins with a 16-bit input vector, which is then split into two 8-bit segments. These segments are then combined with a 16-bit PBO register to produce a 16-bit output vector. The final output is labeled 'PBO'.

□ □

?ALIAS □□□□□

[□□]

□□□□□□ □ □□□□ □□□ □

[□□]

□□□□□□□□□□□□□□□□□□□□

[□□]

□□□□□□□□□□□□□□□□□□□□
□□□□□□□□□□□□□□□□□□□□□□

□□□□□□□□□□□□□□□□ □□□ □□□□□ □□□□□□□□

[□]

□□□□□ □ □□□ □□□□□□□□□□□□□□□□□□□□□□□□□□
>?ALIAS CLEAR RUN


```
[ ]  
[ ]  
  
0000000000 0 000 0000000000000000  
>ALI AS CLEAR RUN
```

```

    _ALI ASU    0 0 0 0 0 0 0 0 0
-----
[ 0 0 ]    0 0 0 0 0 0 0
[ 0 0 ]    0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
[ 0 0 ]    0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
           0 0 0
           0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
[ 0 ]      0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
           >_ALI ASU

```


.....

[illegible]

```

_AT      0 0 0 0 0 0 0 0 0 0 0 0
-----
[ 0 0 ]  0 0 0      0 0 0 0 0 0 0 0 0 0 0
[ 0 0 ]  0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
[ 0 0 ]  0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
          0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
          0 0 0 0 0 0
          0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
[ 0 ]
          0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
          >_AT 1000 2000, 10
          .....
          >_AT 1000 2000

```


BELL □ □ □

□ □ □ □ □ □

□ □ □ □ □ □ □ □ □ □ □ □

□ □ □ □ □ □ □ □ □ □ □ □

□
□ □ □ □ □ □ □ □ □ □
>BELL

BELL □ □ □

[□ □] □ □ □ □
[□ □] □ □ □ □ □ □ □ □ □ □
[□ □] □ □ □ □ □ □ □ □ □ □
[□]
 □ □ □ □ □ □ □ □ □ □
 >BELL

[illegible]

CAT □□□□□□□□

[□□]

□□□ □ □□□□□ □

[□□]

□□□□□□□□□□□□

[□□]

□□□□□□□□□□□□□□□□

[□]

□□□□□ □□□□□□□□ □□□□□□□□□□
>CAT FI LE DAT

The diagram consists of a 2x2 grid of boxes on the left, enclosed in dashed lines. To its right is a long horizontal row of 20 individual boxes.

[illegible]

68


```

_CMD      00000000
-----
[ ] [ ]  0000 0 000000000000 000 0
[ ] [ ]  00000000000000000000
[ ] [ ]  000000000000000000000000
          0000000000000000000000000000000000000000000
          000
          0000000000000000000000000000000000000000000
          0000000000000000000000000000000000000000000
          000000000000000000000000
[ ] [ ]
          000000000000 000000000000000000
          >_CMD CLEAR

```


?COV □ □ □ □ □ □ □ □ □ □

□ □ □ □ □ □

□ □ □

□ □ □

□
□ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □
>?COV
COV H 10000 H 10FFF

```

cov      0 0 0 0 0 0 0 0 0 0 0 0
-----
[ 0 0 ]  0 0 0 0
[ 0 0 ]  0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
[ 0 0 ]  0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
[ 0 ]
      0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
      >cov

```


ECHO □□□□□□

[□□] □□□□ □□□□□□ □□□□

[□□] □□□□□□□□□□□□□□□□□□□□

[□□] □□□□□□□□□□□□□□□□□□□□□□□□
 □□□□ " □□□□□□□□□□□□□□□□□□□
 □□□ □□ □□□□□□□□□□□□

 □□□□□□□□□□□□□□□□ □□□□□ □□□□□□□□□ □□□□□□
□□□□□□

[□]
□□□□□□□□□□
>ECHO "<THIS IS THE MESSAGE>\n"

□□□□□□□□□□□□□□□□□□□□□□□□
>_ECHO
(ECHO) □□□□□□□□□□□□□□□□□□□□
 □□□□□□□□□□□□□□□□□□□□□□□□
 □□□□□□□□□□□□□□□□□□□□
> □□□□□□□□□□□□□□□□□□□□□□□□□□

ELOAD □□□□□□□□□□□□

[□□] □□□□□

[□□] □□□□□□□□□□□□□□□□

[□□] □□□□□□□□□□□□□□□□□□□□□□□□□□□□

□□□□□□□□□□□□□□□□□□□□

□□□□□□	□□□□□□□□□□
□□□□□□□□□□	□□□□□□□□□□
□□□□□□□□	□□□□□□□□
□□□□□□□	□□□□□□□□
□□□□□□□	□□□□□□□□
□□□□□□	□□□□□□□□□
□□□□	□□□□□□□□□
□□□□□□□□	□□□□□□□□□
□□□□□□	□□□□□□□□□

□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□
□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□

□□□
□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□

[□]
□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□
□□□□□□□□□□□□□□□□
>ESAVE □□□□□□□□□□□□□□□□
>Q

□□□□□□□□□□□□□□□□

>ELOAD □□□□□□□□□□□□□□□□□□□□□□□□□□□□

$$\left[\begin{array}{cc} \square & \square \end{array} \right]$$
[illegible]

10

 $\left[\begin{array}{c} - \\ \square \\ - \end{array} \right]$

```

>ESAVE          0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
>Q

```

>LOAD

EXT □□□□□□□□□□

[□□]

(1) □□□

□ □□□□□□□□□□□□□□ □
□ □□□□□□□□□□□□□ □
□ □□□□□□□ □ □□ □
□ □□□□□□□□□□□□ □
□ □□□□□□□□□□□ □
□ □□□□□□□□□□ □

(2) □□□

□□□□□□□□□□□□□□—□□□□□□□□□□□□ □□□□
□ □□□□□□□□ □

□ □□□□□□□□□□□

[□□]

□□□□□□□□□□□□□□□

[□□]

□□□□□□□□□□□□□□□□□□□□

□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□

□□□□□□□□□□□□□□□□□□□□□□
□□□ □□□□□□□□□□□□□□□□□□□□□□□□

□□	□□□□□□□□□□□□□□
□□	□□□□□□□□□□□□□□□□
□	□□□□□□□□□□□□□□□□
□	□□□□□□□□□□
□	□□□□□□□□□□
□	□□□□□□,□□□□□□□□□□

□□□ □□□□□□□□□□□□□□

□□□

□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□
□□□□□□□□□□□□□□□□□□□□□□

[□]

□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□
>EXT DAY 1000 D/10 C/W

_EXT □ □ □ □ □ □ □ □ □ □

□ □ □ □ □ □

□ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □

□ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □

□
□ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □
>_EXT

A diagram showing a 2x2 grid of dashed lines. Inside each of the four dashed squares is a smaller solid rectangle, representing a 2x2 grid of solid rectangles.

(2) $\square \square \square \square \quad \square \square \square \square \square \quad \square \square \square \square \square \quad \square \square \square \square \square \square \quad \square \square \square \square \square \square$

[illegible]

A diagram showing a 2x2 grid. The top row contains two rectangles, and the bottom row is empty.

□ □

[illegible][illegible][illegible]

□ □

0 d i g i t s 0 0 d i g i t s 0 E | e - | + d i g i t s

□ □ □ □ □ □ □ □ □ □ □ □

□ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □

[illegible]

□ □

[illegible][illegible]

□ □

10

(1) □□□□□□□□□□□□□□□□□□□□□□□□□□□□

>FCVT 0.1 1.0E-1

```
float = 3DCC CCCC (1.000000E-001)
```

```
double e= 3FB9 9999 9999 999A ( 1. 000000E- 001)
```

EW200 3DCC CCCC

~~~~~ □ □ □ □ 200 □ □ 3000 202 □ □ 0000 □ □ □ □ □ □

(2) □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □

>FCVT 3E4C 0000

```
float = 0.200000( 2.000000E-001)
```

3FC9 9999 9999 999A

                                            □ □ □ □ □ □ □ □ □ □ double e □ □ □ □ □ □ □ □ □ □

```
double e= 0.200000( 2.000000E- 001)
```



FK      □□□□□□□□□□

---

[□□]

□□   □□□□   □□□□□

□□□□   □   □□□□□□□□□□□□□□  
                 □□□□□□□□□□□□  
□□□□□□   □□□□□□

[□□]

□□□□□□□□□□□□□□□□□□

[□□]

□□□□□□□□□□□□□□□□□□□□□□□□□□□□

□□□□□□□□□□□   □□□□□□□□

□□□□   "   □□□□□□□□□□□□□□□□□□□□□□□□  
□□□□□□□□   □□   □□□□□□□□□□

□□□□□□□□□□□□□□□□□□

□□□

□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□  
□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□  
□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□

[□]

□□□   □   □□   □□□□□□□□   □□□□□□□□  
>FK 1 "GO 2000¥"

?FK      □□□□□□□□□□□□□□□□

[□□]

□□□ □ □□□□ □□□ □

□□□□ □ □□□□□□□□□□□□  
□□□□□□□□□□□□

[□□]

□□□□□□□□□□□□□□□□□□□□□□

[□□]

□□□□□□□□□□□□□□□□□□□□□□□□

□□□□□□□□□□ □□□□□□

□□□□□□□□□□□□□□□□□□□□□□□□

[□]

□□□□ □□□□□□□□□□□□□□□□

➤FK 10

FK 10 "GO 2000¥"







GORES      □ □ □ □ □ □ □

A diagram showing two groups of objects. On the left, there are two small rectangles enclosed by dashed brackets, representing a group of 2. On the right, there are five small rectangles enclosed by solid brackets, representing a group of 5.

The diagram shows a group of 2 objects (represented by small squares) enclosed in square brackets  $[ ]$ . To the right of this group is a horizontal row of 12 individual objects, also represented by small squares.

The diagram consists of a 2x2 grid of boxes on the left, enclosed in dashed lines. To its right is a long horizontal row of 20 individual boxes.

□ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □

111

[illegible][illegible]

□ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □

□ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □

>GORES





```

    _HS      0000000000
-----
[ 00 ]      0000
[ 00 ]      0000000000000000
[ 00 ]      000000000000000000000000000000000000
[ 0 ]
      0000000000000000
      >_HS

```

I CESRC □□□□□□□□□□□□□□□□

[□□] (1) □□□□□□ □□□□□□ □□□□□□□□□□  
□□□□□□ '\*'□□□□□□□□□□□□□□□□

[□□] □□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□

[□□] □□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□  
□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□

□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□  
□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□  
□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□

□□□□□□□□□□ "I CESRC"□□□□□□□□□□□□□□□□□□□□

(1) □□ (1)□□□□

□□□□□□ □□□□□□ □□□□□□□□□□□□

□□□□□□□□□□□□□□□□□□□□□□□□  
□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□  
□□□□□□ '\*'□□□□□□□ I CESRC□□□□□□□□□□□□□□□□  
□□□□□□□□□□□ 200□□□□□□□□□□□□□□□□□□□□

[□□]

□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□

>I CESRC

I CESRC "A: ¥HBYC A: ¥HBYASM ;□□□□□□□□□□□□□□□□  
~~~~~

□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□

>I CESRC B: ¥I CEYDEMO C: ¥SRC ;□□□□□□□□□□□□□□□□
~~~~~

>LOAD B: ¥I CEYDEMO/SAMPLE.ABS ;□ B: ¥I CEYDEMO/SAMPLE.C□□□□□□□□□□  
;□□□□□□□□□□□□

□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□

>I CESRC A: ¥HBYC A: ¥HBYASM ;□□□□□□□□□□□□□□□□□□□□

>I CESRC \* B: ¥I CEYDEMO ; '\*'□ A: ¥HBYC□ A: ¥HBYASM□□□□□□□□  
~~~~~ ;□□□□ B: ¥I CEYDEMO□□□□□□□□□□□□


INSPECT

(1)

(2)

(1)

(2)

Repair nt

(SH long, unsigned long, SH short, unsigned short)

4.4.18

tag_data

INSPECT ((char [4])&word_data)

xdata_t

INSPECT (*(struct xdata_t *)0xA000)

LINE 0 0 0 0 0 0

[0 0]

0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
 0 0 0 0 0 0 0 0

[0 0]

0 0

[0 0]

0
0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0

◀ 0 0 0 0 ▶ 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
 0 0 0 0 0 0 0 0 0 0 0 0
 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
 0 0 0 0 0 0 0 0 0 0 0

0 0 0 0 0 0 0 0 0 0 0 0 ▶ 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
 0 0 0 0 0
 0 0 0 0 0 0 0 0 0 0 0 0 0

[0]

0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
▶ LINE BREQ'D

[illegible]



```

> func_sub()
>LOAD SAMPLE
>AT %unc_sub
>GO
>STEP 8
>LOCAL

```


?LOG □ □ □ □ □ □ □ □ □ □

[□ □]

□ □ □ □

[□ □]

□ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □

[□ □]

□
□ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □

[□]

>LOG LOGSAV

□ □ □ □ □ □ □ □ □ □ □ □

.....

□ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □

>?LOG

□ □ □ □ □ □ □ □ □ □ □ □ □ □

LOG LOGSAV

[illegible]

.....

□ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □

```

MAP      0 0 0 0 0 0
-----
[ 0 0 ]  0 0 0 0
[ 0 0 ]  0 0 0 0 0 0 0 0 0 0 0 0 0 0
[ 0 0 ]  0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
[ 0 ]
      0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
      > MAP

```


$$\left[\begin{array}{c} \square \end{array} \right]$$

112

[illegible][illegible]

□ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □

[illegible]

QUESTION

[illegible][illegible][illegible]

10

[illegible]
$$\left[\begin{array}{c} \text{---} \\ \text{---} \\ \text{---} \end{array} \right]$$

□ □

114

.....

A diagram of a 2D array with 2 rows and 20 columns. The first two columns are enclosed in a dashed box, representing a 2x2 subarray.

REM 5742 0 0 0

```

_PASS      0 0 0 0 0 0 0 0 0 0 0 0
-----
[ 0 0 ]    0 0 0 0 0
[ 0 0 ]    0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
[ 0 0 ]    0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
[ 0 ]
           0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
           >_PASS

```


?PER □ □ □ □ □ □ □ □ □ □ □ □ □ □

A diagram showing two groups of objects. On the left, there are two small vertical rectangles enclosed within a dashed-line bracket. On the right, there are four small vertical rectangles enclosed within a solid-line bracket.

The diagram consists of two parts. On the left, there is a 2x2 grid of boxes, with each box containing a single dot. This grid is enclosed in a larger dashed rectangular frame. To the right of this grid is a long horizontal row of 18 individual boxes, each containing a single dot.

The diagram consists of a 2x2 grid of boxes on the left, followed by a long horizontal row of 20 individual boxes. The 2x2 grid is enclosed in a dashed square frame. The long row of boxes is a single horizontal line of 20 boxes.

□ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □

>?PER

```
PER M1 H 010000 H 01FFFF
```

PER M2 H 002000 H 0020FF

.....

A horizontal chain of 20 small squares representing lattice sites. The first two squares on the left are grouped together by a dashed rectangular box, indicating a subsystem. The remaining 18 squares are not enclosed, representing the rest of the system.

[illegible]
$$\left[\begin{array}{c} \square \\ \square \end{array} \right]$$

quit □□□□□□□□

□□ □□□□

□□ □□□□□□□□□□□□□□□□

□□ □□□□□□□□□□□□□□□□

□

>quit

$$\left[\begin{array}{c} \text{---} \\ \text{---} \\ \text{---} \end{array} \right]$$

```

REM      0 0 0 0 0
-----
[ 0 0 ] 0 0 0      0 0 0 0 0 0 0
[ 0 0 ] 0 0 0 0 0 0 0 0
[ 0 0 ] 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
      0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
      0 0 0
      0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
[ 0 ]
      >REM  EXECUTE GO
      >GO
      >REM  TH S I S COMENT !

```

RESET □□□□□□□

[□□]

□□□□□ □□□□□□□□

□□□□□□ □ □□□□□□□□□□□□□□□□□□□

[□□]

□□□□□□□□□□□□

[□□]

□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□
□□□□□□□□

□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□
□□□□□□

[□]

□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□
>RESET 100

SAVE □□□□□□□□

[□□]

□□□□□□□□□□□□ □□□□□□ □□□□□

□ □ □□□□□□□□□□□□□□
□ □ □□□□□□□□□□

[□□]

□□□□□□□□□□□□□

[□□]

□□□□ <□□□□>□□□□□□□□□□□□

□□□□□□□□□□□□□□□□□□□□□□□□□□□□

□ □□□□□□□□□□□□□□□□
 □□□□□

□ □□□□□□□□□□□□□□□□

[□]

□□□□□□□□□□□□□□□□□□□□□□□□
>SAVE PROG1. SAV 2000 3000

□□□□□□□□□□□□□□□□□□□□□□□□
>SAVE PROG2. SAV 2000 ¥5000

$$\left[\begin{array}{cc} \square & \square \end{array} \right]$$

```

    0 0 0 0 0 0
      0 0 0
        0 0 0
          0 0 0

```

```

0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
0 0 0 0 0 0 0 0 0

```

```

[ ]
[ ]

```

```

0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
>SET CLOCK/U MODE/1

```

?SET □ □ □ □ □ □ □ □ □ □ □ □

[□ □] □ □ □ □

[□ □] □

[□ □] □

□
□ □

[□]

□ □

➤ SET

□ □

□ □

➤ SET > SET. SAV □

□ □ □ □

× SET. SAV □
 □ □ □ □ □ □ □ □ □ □

 \geq

SMPL □ □ □ □ □ □ □ □ □ □

□ □ □ □ □ □ □

□ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □

□ □ □

□
□ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □
≥ SMPL

SRCH □□□□□□□□□□

[□□] □□□□ □□□□□ □□□□□ □ □□□□□ □□□ □

[□□] □□□□□□□□□□□□□□□□□□□□□□□□□□□□

[□□] □□□□ ◀□□□▶□□□□□□□□□□□□□□□□□□□□□□□□□□□□

□□□
□□□
□□□

□□□

[□]

□□□

>SRCH 1000 ¥100 0

FOUND

| | | | | | | | |
|--------|--------|--------|--------|--------|--------|--------|--------|
| 001000 | 001001 | 001004 | 001005 | 001008 | 001009 | 00100B | 00100F |
| 001010 | 001011 | 001014 | 001015 | 001016 | 001017 | 001018 | 001019 |
| 00101A | 00101B | 00101C | 00101D | 00101E | 00101F | 001020 | 001021 |
| 001022 | 001023 | 001024 | 001025 | 001026 | 001027 | 001028 | 001029 |

□□□ □□□□ □□□□□□□□□□

>SRCH 20000 ¥80 ' 1234'

(1) (1) (1) (1)

(2) □ □ (2) □ □ □ □

STEP

A diagram showing a 2x2 grid of dashed lines. Inside each of the four dashed squares, there is a smaller solid rectangle. The rectangles are positioned such that they are centered within their respective dashed squares.

[illegible]

□ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □

146

[illegible][illegible][illegible][illegible]

□ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □

The image shows a 10x10 grid of squares. The squares are arranged in two main groups: a 10x5 grid on the left and a 10x5 grid on the right. The squares are filled with black in a pattern that resembles a stylized '10' or a specific data visualization. The pattern is as follows:

| Row | Col 1 | Col 2 | Col 3 | Col 4 | Col 5 | Col 6 | Col 7 | Col 8 | Col 9 | Col 10 |
|-----|-------|-------|-------|-------|-------|-------|-------|-------|-------|--------|
| 1 | Black | | | | | Black | Black | Black | Black | Black |
| 2 | Black | | | | | Black | Black | Black | Black | Black |
| 3 | Black | Black | | | | Black | Black | Black | Black | Black |
| 4 | Black | Black | Black | Black | | Black | Black | Black | Black | Black |
| 5 | Black | Black | Black | Black | Black | Black | Black | Black | Black | Black |
| 6 | Black | Black | Black | Black | | Black | Black | Black | Black | Black |
| 7 | Black | Black | Black | Black | | Black | Black | Black | Black | Black |
| 8 | Black | Black | Black | Black | | Black | Black | Black | Black | Black |
| 9 | Black | Black | Black | Black | | Black | Black | Black | Black | Black |
| 10 | Black | Black | Black | Black | | Black | Black | Black | Black | Black |

□ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □

[illegible]

| | |
|-------------------|-----------------------------|
| □ □ □ □ , □ □ □ □ | □ □ □ □ □ □ □ □ □ □ □ □ □ □ |
| | □ □ □ □ □ □ □ |
| □ □ □ □ , □ □ □ □ | □ □ □ □ □ □ □ □ □ □ □ □ □ □ |
| | □ □ □ □ □ □ |
| □ □ □ □ , □ □ □ □ | □ □ □ □ □ □ □ □ □ □ □ □ □ □ |
| | □ □ □ □ □ □ |

$$\left[\begin{array}{c} \text{---} \\ \text{---} \\ \text{---} \end{array} \right]$$
[illegible]

?TG □ □ □ □ □ □ □ □

[□ □]

□ □ □ □ □ □

□ □ □ □ □ □ □ □ □

[□ □]

□ □ □ □ □ □ □ □ □ □ □ □

[□ □]

□ □ □ □ □ □ □ □ □ □ □ □ □ □

□ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □

[□]

□ □ □ □ □ □ □ □ □ □ □ □ □ □

>?TG

TG0 DA=2000 D=20 C=W

TG1 DA=1000

□ □ □ □ □ □ □ □ □ □ □ □ □ □

[illegible][illegible]
$$\left[\begin{array}{cc} - & - \\ \square & \square \\ - & - \end{array} \right]$$

□ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □

A diagram showing a large rectangle divided into four smaller rectangles by a horizontal line and a vertical line. The rectangles are arranged in a 2x2 grid.

[illegible]

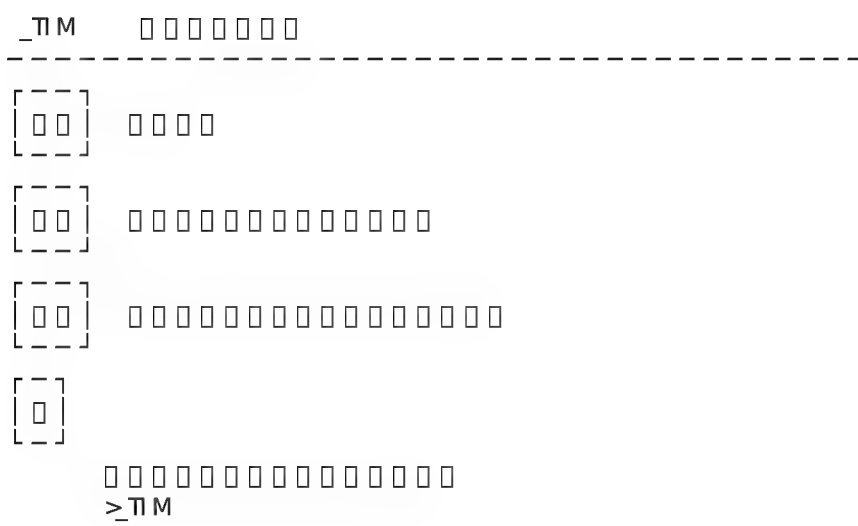
□ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □

[illegible]

>T1 M CN PA/ 2000 CFF PA/ 3000

□ □ □ □ □ □ □

□ □ □ □ □ □ □



?TRC □ □ □ □ □ □ □

[□ □]

□ □ □ □

[□ □]

□ □ □ □ □ □ □ □ □ □ □ □ □

[□ □]

□ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □

□ □

[□]

□ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □

>?TRC

TC0 PA=H 000600 C=R PF

TRC SMP TC0 -

CLY/0 FUL/BRK

_TRC □ □ □ □ □ □ □ □

□ □ □ □ □ □

□ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □

□ □ □

□
□
≥_TRC

VRFY □□□□□□□□□□

□□ □□□□□□□□□□□□□□ □□□□□□

□ □ □□□□□□□□□□□□□□□□
□ □ □□□□□□□□□□
□ □ □□□□□□□□□□

□□ □□□□□□□□□□□□□□

□□ □□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□
□□□□□□□□

□□□□□□□□□□□□□□□□□□□□□□□□□□□□

□ □□□□□□□□□□□□□□□□□□□□
□□□□

□ □□□□□□□□□□□□□□

□ □□□□□□□□□□□□□□□□□□□□

□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□
□□□□□□

□

□□□□□□□□□□□□□□□□□□□□□□
>VRFY PROGRAM

